

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Appln. No: To Be Assigned
Applicant: Koji Takinami et al.
Filed: Herewith
Title: VOLTAGE-CONTROLLED OSCILLATOR, RADIO COMMUNICATION
APPARATUS AND VOLTAGE-CONTROLLED OSCILLATION METHOD
TC/A.U.:
Examiner:

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Pursuant to 37 C.F.R. §§ 1.97 and 1.98 and to the duty of disclosure set forth in 37 C.F.R. § 1.56, the Examiner in charge of the above-identified application is requested to consider and make of record the references listed on the PTO 1449 (RP) submitted herewith. A copy of each of the listed references is also enclosed.

Although the information submitted herewith may be "material" to the Examiner's consideration of the subject application, this submission is not intended to constitute an admission that such information is "prior art" as to the claimed invention.

In accordance with 37 C.F.R. § 1.97(g), the filing of this Information Disclosure Statement shall not be construed to mean that a search has been made.

Reference JP 2001-352218 is not in the English language, but an English translation and abstract are attached to the copy of the reference submitted herewith. Furthermore, a "concise explanation" under 37 CFR Section 1.98(a)(3) is set forth in the background portion of the above specification on page 4.

Reference "Prospects of CMOS Technology for High-Speed Optical Communication Circuits" is cited in the background portion of the above specification on page 6.

This Information Disclosure Statement is being filed concurrently with the above-referenced application.

Respectfully submitted,



Allan Ratner, Reg. No. 19,717
Attorney for Applicants

AR/dlm

Enclosures: PTO Form 1449
(2) References

P.O. Box 980
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(610) 407-0700

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Kathleen Libby

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(Use as many sheets as necessary)

SHEET 1 of 2

Complete if Known

Application Number	To Be Assigned
Filing Date	Herewith
First Named Inventor	K. Takinami et al.
Art Unit	
Examiner Name	
Attorney Docket No.	MTS-3475US

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FOREIGN PATENT DOCUMENTS						
Examiner Initials*	Cite No. ¹	Foreign Patent Document	Publication Date (MM-DD-YYYY)	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T ⁶
		Country Code ³ - Number ⁴ - Kind Code ⁵ (If known)				
		JP 2001-352218 (w/English abstract)	12/21/2001	Nippon Telegr & Teleph Corp	Entire Document	<input checked="" type="checkbox"/>
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Examiner
Signature

Date	
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¹Applicant's unique citation designation number (optional).

²See Kind Codes of USPTO Patent Documents at www.uspto.gov or MPEP 901.04.

³Enter Office that issued the document, by the two-letter code (WIPO Standard St.3).

⁵Kind of document by the appropriate symbols as indicated on the document.

⁶Applicant is to place a check mark here if English is the language of the document.

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The collection of information is required by 37 CFR 1.97 and 1.98. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 2 hours to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. **SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.**

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Substitute for Form 1449B/PTO

**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**

(Use as many sheets as necessary)

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Application Number To Be Assigned

Filing Date Herewith

First Named Inventor K. Takinami et al.

Art Unit

Examiner Name

Attorney Docket No. MTS-3475US

SHEET 2 of 2

NON-PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published	T ²
		B. Razavi, "Prospects of CMOS Technology for High-Speed Optical Communication Circuits", IEEE Journal of Solid-State Circuit, vol. 37, no. 9, September 2002, pp. 1135-1144).	<input type="checkbox"/>
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*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to Applicant.

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